

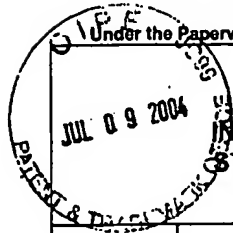
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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Substitute for Form 1449A/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

## Complete if Known

Application Number	10/810,095
Filing Date	03/28/2004
First Named Inventor	Abdelatif Bellaouar, et al.
Group Art Unit	2817
Examiner Name	TBD
Attorney Docket No.	TI-36131

Sheet	2	of	2
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## OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Exam. Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
<i>[Signature]</i>	CA	"A Filtering Technique to Lower Oscillator Phase Noise", Emad Hegazi, et al., 2001 IEEE International Solid-State Circuits Conference, ISSCC 2001/Session 23/Analog Techniques/23.4, pp. 364, 365, 465	
<i>[Signature]</i>	CB	"A 50GHz VCO in 0.25µm CMOS", HongMo Wang, 2001 IEEE International Solid-State Circuits Conference, ISSCC 2001/Session 23/ Analog Techniques/23.8, pp. 372, 373	
<i>[Signature]</i>	CC	"A Study of Phase Noise in CMOS Oscillators", Behzad Razavi, IEEE Journal of Solid-State Circuits, Vol. 31, No. 3, 03/1996, pp. 331-343	
<i>[Signature]</i>	CD	"Electromagnetically Shielded High-Q CMOS-Compatible Copper Inductors", Hongsul Jlang, et al., IEEE International Solid-State Circuits Conference, ISSCC 2000/Session 19 / TD: High-Frequency Wireless / Paper WA 19.7, pp. 330-331	
	CE		
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	CP		
	CQ		
	CR		

Examiner Signature	<i>MLW</i>	Date Considered	7/29/05
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Alexandria, VA 22313-1450.

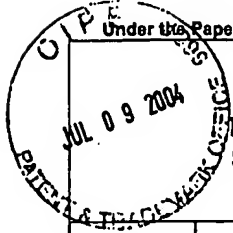
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PTO/SB/08A (08-00)

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Substitute for Form 1449A/PTO			Complete If Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			Application Number	10/810,095
			Filing Date	03/26/2004
			First Named Inventor	Abdellatif Bellaouar, et al.
			Group Art Unit	2817
			Examiner Name	TBD
Sheet 1 of 2			Attorney Docket No.	TI-36131

U.S. PATENT DOCUMENTS						
Exam. Initials •	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
	AA					
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FOREIGN PATENT DOCUMENTS								
Exam. Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>2</sup> (if known)				
	BA							
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<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the Indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Abdellatif Bellaouar, et al.

Docket No.: TI-36131

Serial No.: 10/810,095

Examiner: TBD

Filed: 03/26/2004

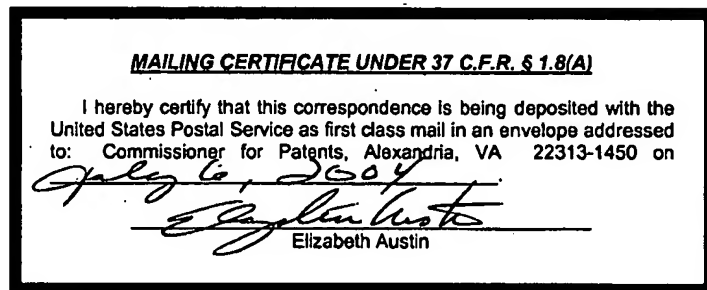
Art Unit: 2817

For: Low-Current, Area-Efficient and Flicker Noise  
Free Bias CMOS Voltage Control Oscillator

Conf. No.: 8199

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Alexandria, VA 22313-1450



Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the listed references are enclosed.

This information disclosure statement is being mailed before receipt of a first office action on the merits in accordance with 37 C.F.R. 1.97(b)(3).

No fees are due, however, should the Office decide otherwise, please charge the necessary fee of \$180 to the deposit account of Texas Instruments Incorporated, Account Number 20-0668.

Respectfully submitted,

Ronald O. Neerings  
Attorney for Applicants  
Reg. No. 34,227

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